



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

JOHN F. ZUMKEHR, ET AL.

Application No.: 10/714,075

Filed: November 14, 2003

For: **Internal Voltage Reference for Memory Interface**

Art Group: 2819

Examiner: Daniel D. Chang

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

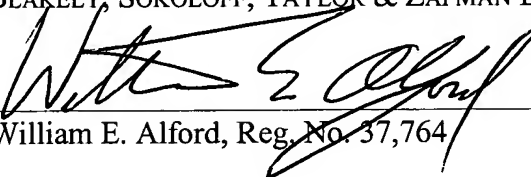
In accordance with the duty of disclosure, enclosed are the Information Disclosure Statement by Applicant (form PTO/SB/08 or PTO-1449) and copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed), which are being submitted on or before payment of the Issue Fee. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: September 26, 2005

  
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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

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of

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**Complete if Known**

Application Number	10/714,075
Filing Date	November 14, 2003
First Named Inventor	John F. Zumkehr
Art Unit	2819
Examiner Name	Daniel D. Chang
Attorney Docket Number	42P17976

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
		US-6,316,980	11-13-2001	Vogt et al.	
		US-6,456,544	09-24-2002	Zumkehr	
		US-6,581,017	06-17-2005	Zumkehr	
		US-6,617,895	09-09-2003	Zumkehr et al.	
		US-6,629,225	09-30-2003	Zumkehr	
		US-2003/0235084	12-25-2003	Zumkehr et al.	
		US-2004/0123207	06-24-2004	Zumkehr et al.	
		US-6,864,731	03-08-2005	Zumkehr et al.	
		US-6,918,048	07-12-2005	Zumkehr	
		US-6,922,077	07-26-2005	Chandler et al.	
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
		Country Code <sup>2</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				

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<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<b>Complete if Known</b>	
				Application Number	10/714,075
				Filing Date	November 14, 2003
				First Named Inventor	John F. Zumkehr
				Art Unit	2819
				Examiner Name	Daniel D. Chang
Sheet	2	of	2	Attorney Docket Number	42P17976

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		T <sup>2</sup>
		JEDEC STANDARD, DDR2 SDRAM SPECIFICATION, JESD79-2, September 2003, JEDEC Solid State Technology Association, Electronic Industries Alliance		
		JEDEC STANDARD, Double Data Rate (DDR) SDRAM Specification, JESD79C (Revision of JESD79B), March 2003, JEDEC Solid State Technology Association, Electronic Industries Alliance		

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